



Docket No.: 24657US6

OBLON
SPIVAK

ATTORNEYS AT LAW

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

RE: Inventor: Takahito NAKANO, et al.
Patent No.: 7,346,712
Issued: March 18, 2008
Group Art Unit: 2181
Examiner: FRANKLIN, RICHARD B
For: SEMICONDUCTOR INTEGRATED CIRCUIT
APPARATUS AND CIRCUIT BOARD AND
INFORMATION READOUT METHOD

SIR:

Attached hereto for filing are the following papers:

REQUEST FOR CERTIFICATE OF CORRECTION
CERTIFICATE OF CORRECTION FORM PTO 1050

Our online credit card payment in the amount of \$0.00 is being made covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030.

Respectfully submitted,

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